

**Title: 4<sup>th</sup> IEEE International Workshop on High-Speed Network and Computing Environment (HSNCE 2013)**

**Workshop Organizers:**

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**Goal of the Workshop:**

The aim of this workshop is to discuss and advance the state-of-the-art, research and development in the area of data-intensive applications and systems to execute such applications on high-speed network and computing environment, and to promote the study of both fundamental and practical aspects of design of such applications and systems. The workshop addresses researchers from different disciplines in academia and industry, as well as practitioners, who share interests in system design on high-speed network and computing environment. The focus will be on techniques and experiences drawn from current system design and scheme practice, as well as on emergent topics.

**Theme of the Workshop:**

In the last decade, high-speed networks and high-performance computing resource have considerably and constantly increased. Consequently, this evolution has fostered the emergence of global-scale applications exchanging tremendous amount of data, highly demanding in terms of computing and whose complexity seems limitless. This is noticeably the case for Big Data mining in societal applications, complex systems simulation or multidimensional datasets visualization in sciences, to cite but a few. However, this technological development comes with a cost, e.g. regarding energy efficiency, and its potential is certainly not endless. Under this thrust, rational and efficient utilization of resources became an important challenge in network management. In this direction, infrastructure virtualization is a physical abstraction that permits a better exposition of the application requirements, of the network capacity and of the resource availability, along with an interactive communication between these entities. The theme of this workshop is to highlight the advantages and the flexibility of cloud networking to support Big Data applications, with particular focus on Software Designed Networks and middleware design.

**Scope of the Workshop (Call-for-Papers):**

Any submission whose content is relevant to the area of data-intensive applications and systems to execute such applications on high-speed network and computing environment will be considered, but submissions whose subject is related to one of the following topics will be particularly welcome:

- Large-scale applications
- Applications in world-wide computing environment

- Visualization of large datasets
- Large-scale data sharing scheme
- Large-scale and distributed storage
- Big Data analytics and knowledge management
- Dynamic resource management
- Network middleware, cloud architecture, and computing
- Energy efficiency in high-speed networks and computing environments
- Measurements issues in high-speed networks and large-scale distributed applications
- SDN for global-scale applications
- Middleware design for demanding application over federated testbeds.

**Program Committee:** (approval pending)

- Yoshiyuki Asai, OIST, Japan
- Annemie van Hirtum, Grenoble University, France
- Matthieu Imbert, ENS-Lyon, France
- Khoji Koyamada, Kyoto University, Japan
- Jysoo Lee, KISTI, Korea
- Kazunori Nozaki, Osaka University, Japan
- Hiroyuki Ohsaki, Osaka University, Japan
- Anne-Cécile Orgerie, CNRS-INRIA, France
- Dimitri Perrin, Dublin City University, Ireland
- Bruno Raffin, INRIA, France
- Naohisa Sakamoto, Kyoto University, Japan
- Eisaku Sakane, NII, Japan
- Shinji Shimojo, Osaka University, Japan
- Shigeo Wada, Osaka University, Japan
- Ray Walshe, Dublin City University, Ireland

**Important Dates:**

Deadline for paper submission: March 20<sup>th</sup>, 2013  
 Notification of acceptance: April 21<sup>th</sup>, 2013  
 Camera-ready due: May 5<sup>th</sup>, 2013

**Submission:**

Papers must be submitted electronically via the HSNCE 2013 Submission Page (<http://myreview.cs.iastate.edu/HSNCE2013>). The format of submitted papers should follow the guidelines for the IEEE conference proceedings. All papers will be carefully reviewed by at least three reviewers. Papers should be no more than 6 pages. Accepted papers will be published in the workshop proceedings of COMPSAC 2013, by the IEEE Computer Society Press. At least one of the authors of each accepted paper must register as a participant of the workshop and present the paper at the workshop, in order to have the paper published in the proceedings.

Expected number of workshop sessions:

2 sessions

**Information on the previously organized Workshops (if any).**

HSNCE2012 (9 submissions, 6 accepted, 66% acceptance ratio)

HSNCE2011 (7 submissions, 4 accepted, 57% acceptance ratio)

HSNCE2010 (8 submissions, 4 accepted, 50% acceptance ratio)